

Abstract of Disclosure

An apparatus for generating wobble clock comprises: a comparing circuit and a phase lock loop. The comparing circuit generates a combining signal according to peak values of the wobble signal and compares the output signal with a reference voltage to output a control signal. The phase lock loop drives the wobble clock to be synchronized with the wobble signal if the protection signal corresponds to a first logic level, and stops driving the wobble clock to be synchronized with the wobble signal if the protect signal corresponds to a second logic level.

Figures

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